AUG 15 2006

REPLACEMENT SHEET

Title: Coherency Techniques for Suspending Execution of a Thread Until a Specified Memory Access Occurs

1st Named Inventor: David L. Hill

Application No.: 10/039,656 Docket No.: 42390P13635

Sheet: 1/12

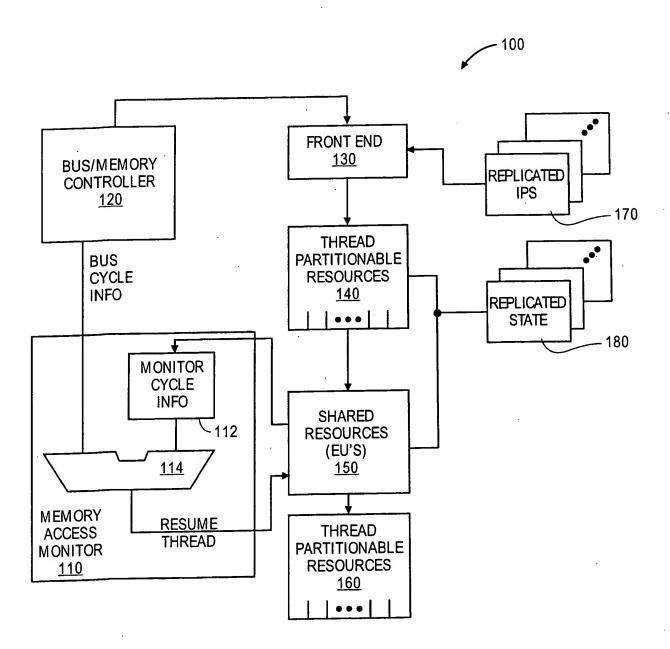


FIG. 1

Title: Coherency Techniques for Suspending Execution of a Thread

Until a Specified Memory Access Occurs 1st Named Inventor: David L. Hill

Application No.: 10/039,656 Docket No.: 42390P13635

Sheet: 2/12

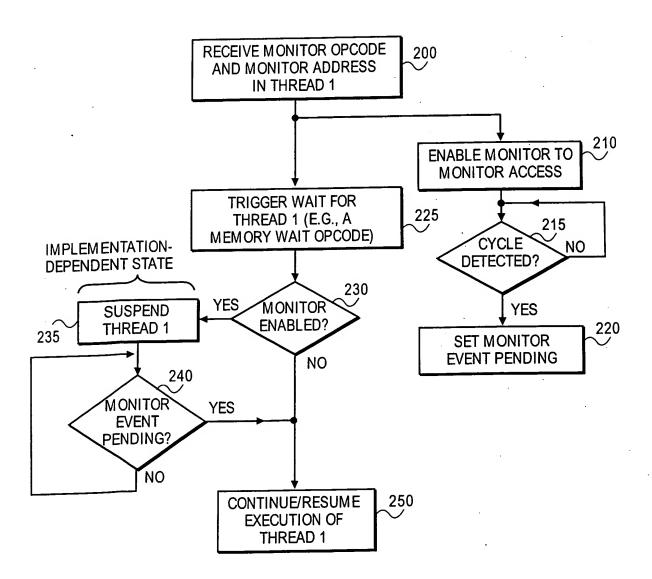


FIG. 2

Title: Coherency Techniques for Suspending Execution of a Thread

Until a Specified Memory Access Occurs
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Application No.: 10/039,656 Docket No.: 42390P13635

Sheet: 3/12

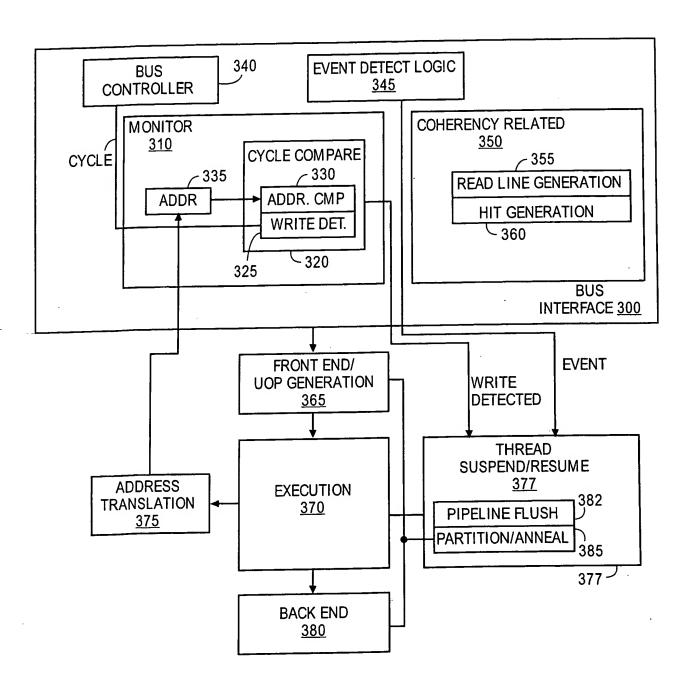


FIG. 3

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Title: Coherency Techniques for Suspending Execution of a Thread

Until a Specified Memory Access Occurs

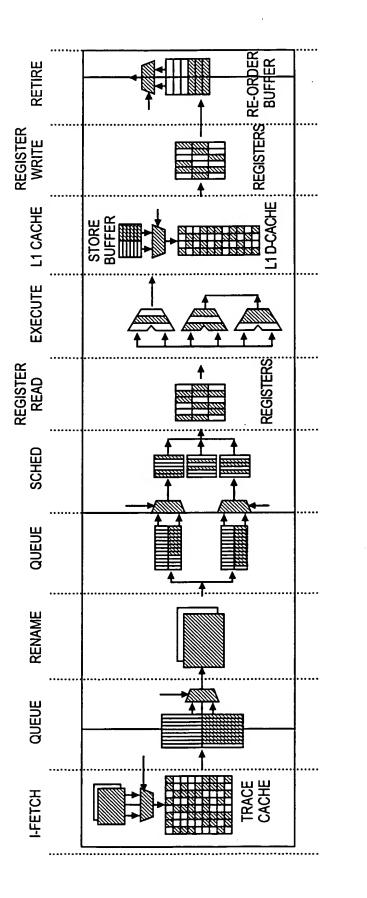
1st Named Inventor: David L. Hill

Application No.: 10/039,656

Docket No.: 42390P1363

Docket No.: 42390P13635

Sheet: 4/12



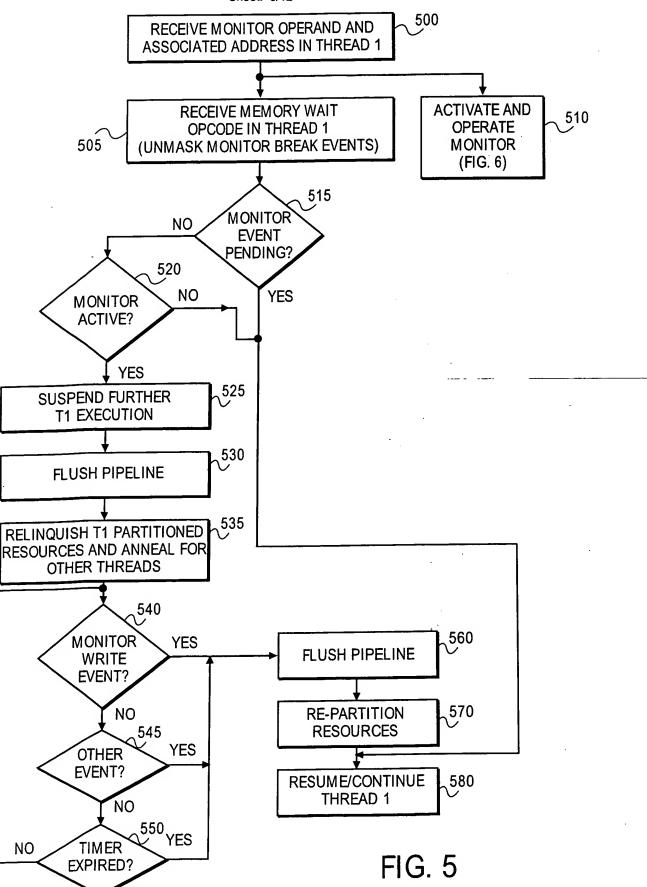
Title: Coherency Techniques for Suspending Execution of a Thread

Until a Specified Memory Access Occurs

1st Named Inventor: David L. Hill

Application No.: 10/039,656 Docket No.: 42390P13635

Sheet: 5/12



Title: Coherency Techniques for Suspending Execution of a Thread

Until a Specified Memory Access Occurs 1st Named Inventor: David L. Hill

Application No.: 10/039,656 Docket No.: 42390P13635

Sheet: 6/12

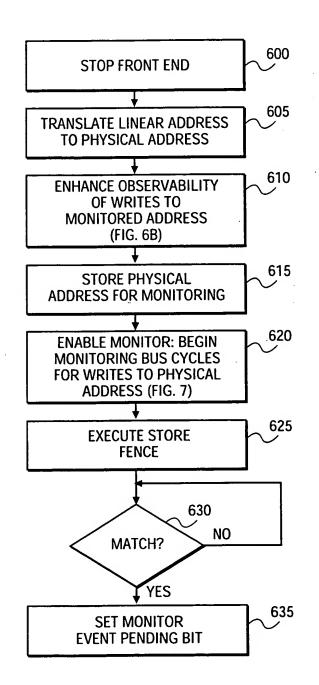


FIG. 6A

Title: Coherency Techniques for Suspending Execution of a Thread Until a Specified Memory Access Occurs

1st Named Inventor: David L. Hill

Application No.: 10/039,656 Docket No.: 42390P13635

Sheet: 7/12

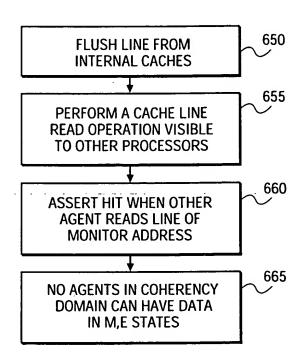


FIG. 6B

Title: Coherency Techniques for Suspending Execution of a Thread

Until a Specified Memory Access Occurs
1st Named Inventor: David L. Hill

Application No.: 10/039,656 Docket No.: 42390P13635

Sheet: 8/12

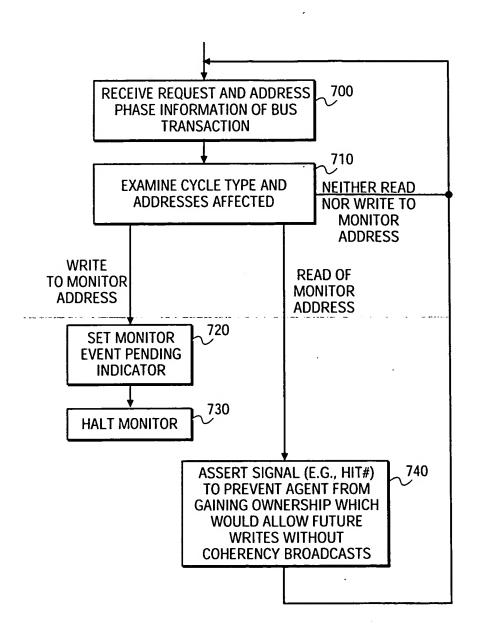


FIG. 7

REPLACEMENT SHEET

Title: Coherency Techniques for Suspending Execution of a Thread

Until a Specified Memory Access Occurs

1st Named Inventor: David L. Hill

Application No.: 10/039,656 Docket No.: 42390P13635

Sheet: 9/12

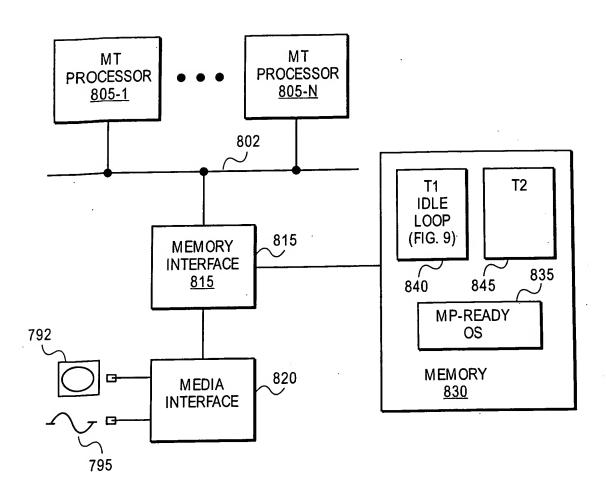


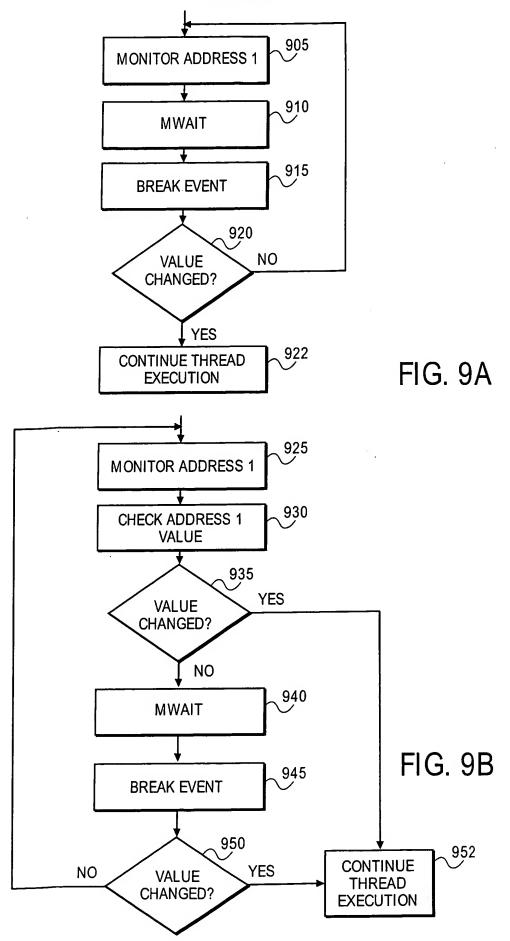
FIG. 8

Title: Coherency Techniques for Suspending Execution of a Thread

Until a Specified Memory Access Occurs 1st Named Inventor: David L. Hill

Application No.: 10/039,656 Docket No.: 42390P13635

Sheet: 10/12



REPLACEMENT SHEET

Title: Coherency Techniques for Suspending Execution of a Thread

Until a Specified Memory Access Occurs

1st Named Inventor: David L. Hill

Application No.: 10/039,656 Docket No.: 42390P13635

Sheet: 11/12

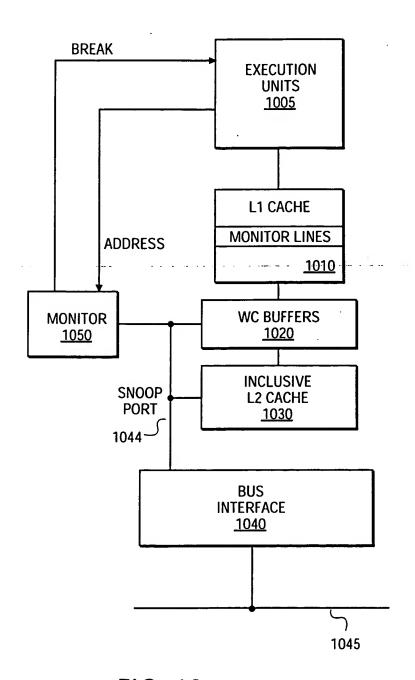


FIG. 10

Title: Coherency Techniques for Suspending Execution of a Thread
Until a Specified Memory Access Occurs
1st Named Inventor: David L. Hill
Application No.: 10/039,656
Docket No.: 42390P1363

Docket No.: 42390P13635

Sheet: 12/12

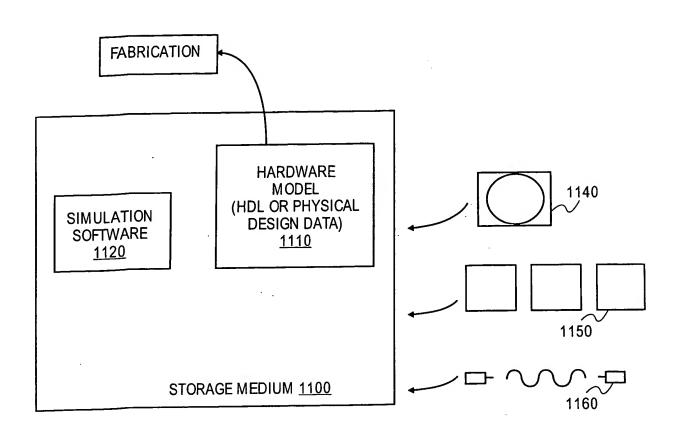


FIG. 11